

ELECTRIC DOUBLE LAYER CAPACITORS

PRODUCT SPECIFICATION

規格書

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CUSTOMER:

(客戶): 志盛翔

DATE:

(日期): 2020-07-20

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DRL 2.7V100F (φ18x60)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER					
CHECKED (审核)					
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CUSTOMER					
APPROVAL (批准)	SIGNATURE (签名)				

		SPECIFICAT	ALTERNATION HISTORY		TORY		
		DRL SERII			ECORDS	•	
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver
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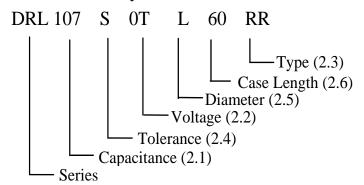
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1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	107
Capacitance (F)	100

2.2 Rated voltage code

Code	0T
Voltage (W.V.)	2.7

2.3 <u>Type</u>

Code	RR
Type	Bulk

2.4 <u>Capacitance tolerance</u>

"S" stands for -20% ~ +50%

2.5 <u>Diameter</u>

Code	L
Diameter	18

2.6 <u>Case length</u> 60=60mm

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3. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C Relative humidity : 25% to 75% Air Pressure : 86kPa to 106kPa

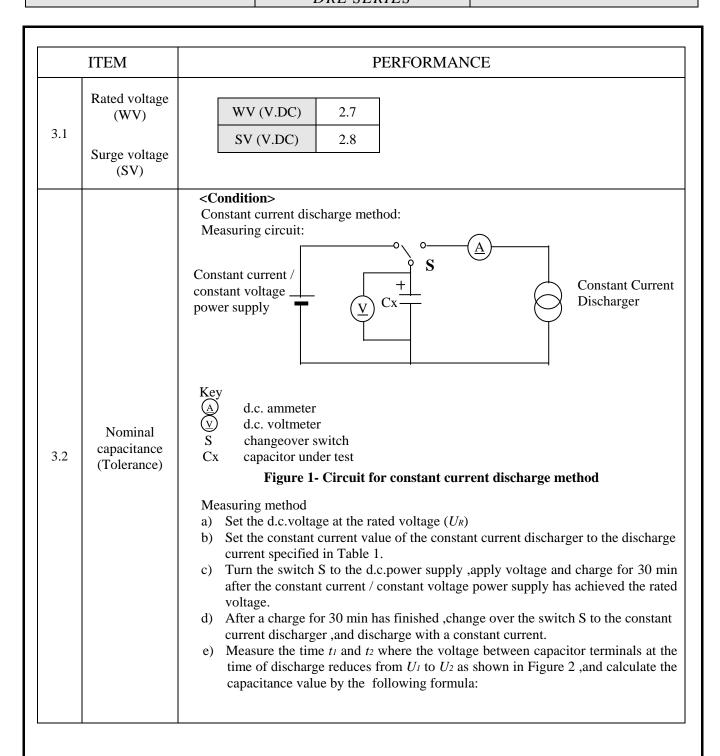
If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature: $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40° C to 70° C.

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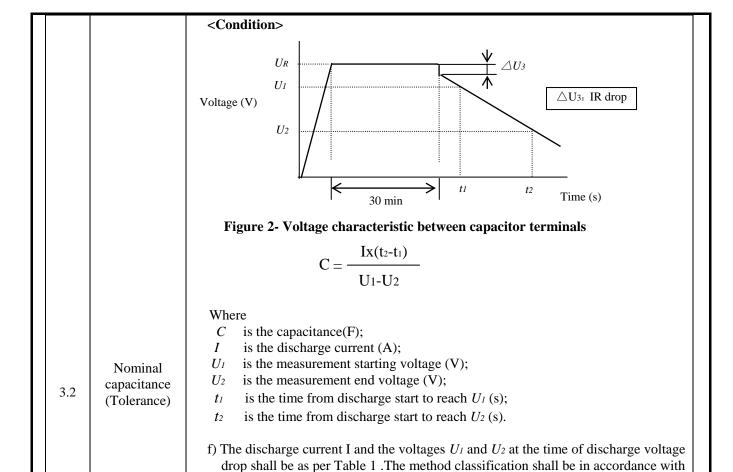


Table 1 – Discharge conditions

the individual standards.

Charge time	30 min
<i>I</i> (mA)	4 x CUR
U_1	The value to be 80% of the charging voltage $(0.8xU_R)$
U_2	The value to be 40% of the charging voltage $(0.4xU_R)$
NOTE CR is the	he rated capacitance in F(Farad),and UR is the rated voltage in V (Volt)

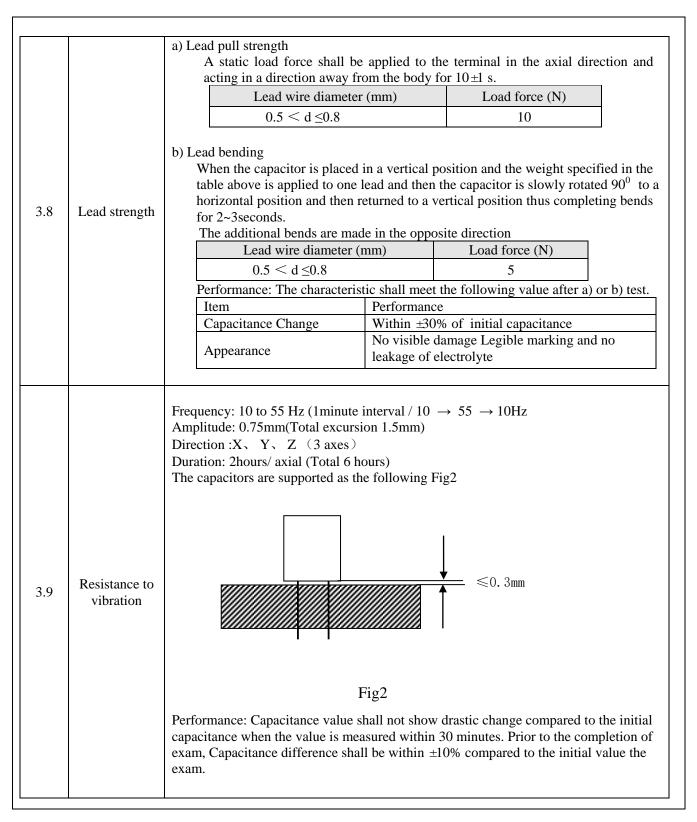
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			ng frequency :1kHz ng temperature:20±2°C	from the surface of	f a sealing resin on the lead		
3.3	ESR	Ra Vol	ass than the initial limit: ted Capacitand tage e (F)	Dimension (D×L, mm)	ESR, AC (m Ω) (max) at 1kHz/20°C		
			V) 2.7 100	18x60	20		
3.4	Leakage current	3. Desist <criteria 0.030<="" i≤="" less="" th="" than=""><th>n the initial limit(25°C ± 2</th><th></th><th></th></criteria>	n the initial limit(25° C ± 2				
		<condition< td=""><td>n> Temperature(°C)</td><td>Item</td><td>Characteristics</td></condition<>	n> Temperature(°C)	Item	Characteristics		
		1	20±2	Capacitance \ ESR			
	Townseton	Tamparatura	Temperature			Δ C/C	Within ±30% of initial capacitance
				2	-40+3	ESR	Less than or equal to 4 times of the value of item 3.3
3.5	characteristic	3	Keep at 15 to 35°C for 15 minutes or more				
		4	70±2	Δ C/C	Within ±30% of initial capacitance		
			70.22	ESR	The limit specified in 3.3		
			.0°C/ ESR 20°C: ESR ratio 20°C: Capacitance chang	*			

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		<criteria></criteria>	
		Item	Performance
		Capacitance Change	Within ±30% of initial capacitance
6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
3.6	test	Appearance	No visible damage and no leakage of electrolyte
		<condition></condition>	
		Humidity Test: The capacitor shall be	exposed for 240±48 hours in an atmosphere of 90~95%RH stic change shall meet the following requirement.
		Humidity Test: The capacitor shall be 40±2°C, the characteri	exposed for 240±48 hours in an atmosphere of 90~95%RH stic change shall meet the following requirement.
		Humidity Test: The capacitor shall be	
	Damp	Humidity Test: The capacitor shall be 40±2°C, the characteri	stic change shall meet the following requirement.
17	Damp heat	Humidity Test: The capacitor shall be 40±2°C, the characteri Criteria> Item	stic change shall meet the following requirement. Performance
3.7		Humidity Test: The capacitor shall be 40±2°C, the characteri <criteria> Item Capacitance Change</criteria>	Performance Within ±30% of initial capacitance

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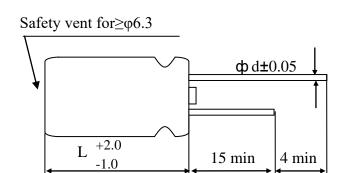
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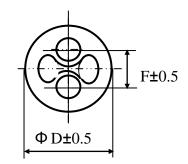
	T	
3.10	Solderability	The capacitor shall be tested under the following conditions: Solder : Sn-3Ag-0.5Cu Soldering temperature: 245±3°C Immersing time : 2.0±0.5s Immersing depth : 1.5~ 2.0mm from the root. Flux : Approx .25% rosin (JIS K5902) in ETHANOL (JIS K1501) Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylaehol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260 ±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass -epoxy board B) Soldering iron method Bit temperature : 350 ±10°C Application time : 3.5 ±0.5 s Heat protector: t=1.6mm glass -epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured: Item Capacitance Change Within ±10% of initial capacitance Appearance No visible damage legible marking and no leakage of electrolyte

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4. Product Dimensions





Unit: mm

φD	18
L	60
F	7.5
φd	0.8

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~" •				

- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction ΔV =IR will happen at the moment of discharge.
- (6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within -30°C~50°C temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- (10) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)

(П) 1	here is	s volta	ge ba	llance	problen	n be	tween	each	capaci	tor unit	during	series	connect	ıon	betwee	ı super	capacito	or.
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